

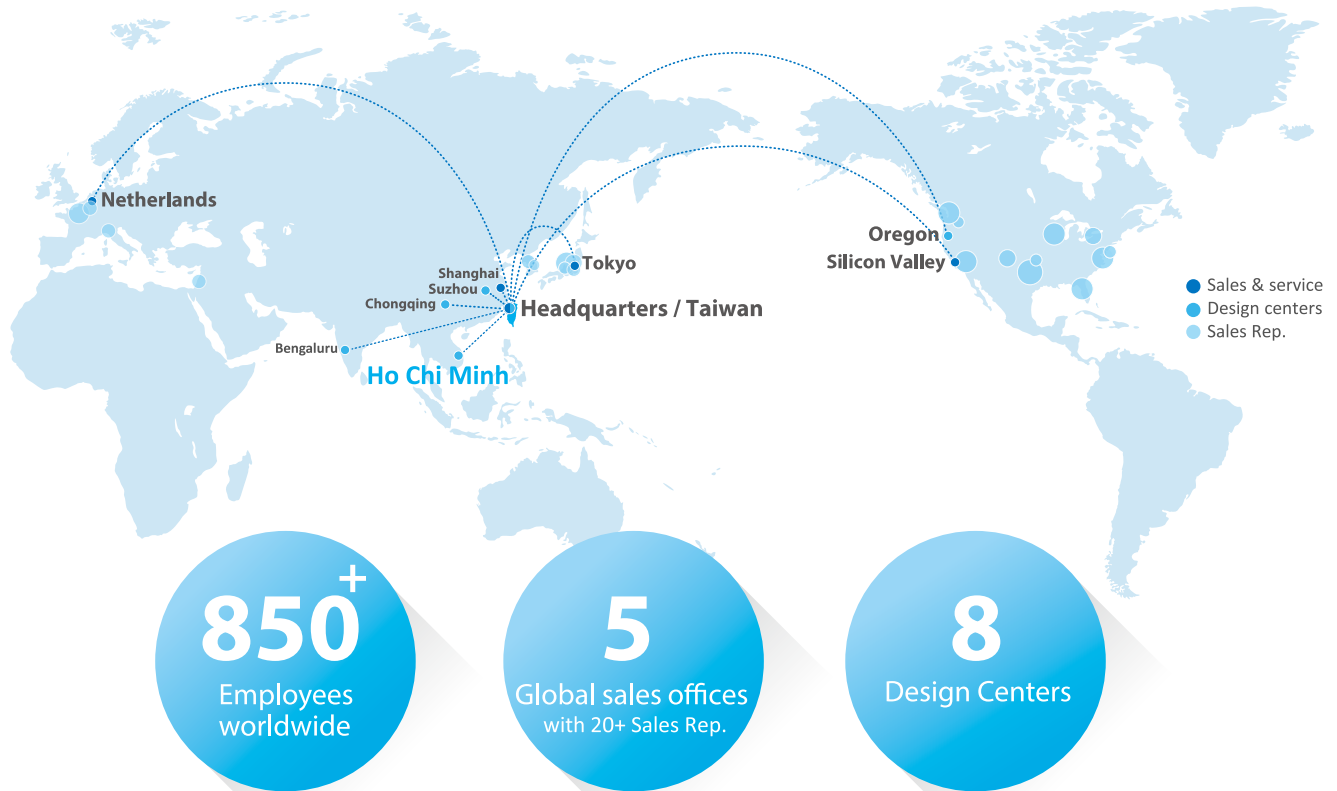
Join our team!

Faraday Technology Vietnam



Faraday Technology Corporation is a leading fabless ASIC and silicon IP (intellectual property) provider. Established in 1993 as the first Asian ASIC vendor, Faraday is one of the few leading ASIC vendors with a comprehensive self-developed IP portfolio.

Faraday has been acknowledged for expertise and capabilities resulting in thousands of successful designs over a wide range of applications including consumer electronics, multimedia, display technology, communications, networking, and peripheral storage. Faraday ships hundreds of millions of ASIC chips worldwide on an annual basis.



0.5μm ~ 7nm
Foundry Solution
• UMC Foundry
• Samsung Foundry

2200⁺
Mass Production Proven Designs
• FPGA to ASIC
• System-level SoC Design

3000⁺
Comprehensive IPs
• SerDes Solution
• High-Speed Interface
• Library & Memory

25⁺
Years' Experience
• Business Continuous Plan
• ISO 26262 Certified

AI, Robotics

3D Sensing, Display

5G, Cloud

Automotive



Email: globalhr@faraday-tech.com
Address: 195 Dien Bien Phu, Binh Thanh District, HCMC, Vietnam
Website: www.faraday-tech.com



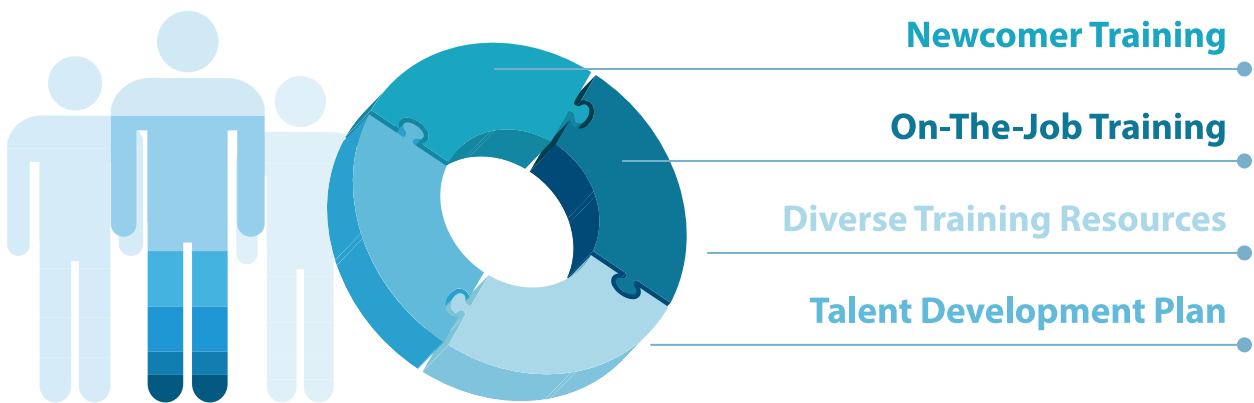
Hot Jobs

Job Positions	Requirements
SOC Physical Design Engineer	<ul style="list-style-type: none"> • Experience in Cadence Innovus flow or Synopsys ICC2 flow • Experience in hierarchical implementation, timing closure, IR drop analysis, crosstalk analysis is a plus • Experience in FinFet process node is a plus
ASIC Consultant Engineer	<ul style="list-style-type: none"> • Experienced in chip design flow and chip implementation flow • Familiar with DFT related flow and utilities is a plus • Experience in FinFet process node is a plus
Memory CAD Engineer	<ul style="list-style-type: none"> • Experience in library or memory characterization is a plus • Familiar with script language such as c-shell and TCL is a plus • Experience in FinFet process node is a plus
Memory Design Engineer	<ul style="list-style-type: none"> • Experience in SPRAM, DPSRAM and 2PRF circuit design • Experience with embedded memory development in FinFET process nodes • Experience in FCI, TCAM, P2P and PDP is a plus • Fluent with SKILL, Perl, Tcl, Python
Standard Cell Library Design Engineer	<ul style="list-style-type: none"> • Solid understanding of device scaling challenges and circuit-process technology interactions applicable for advanced FinFET nodes • Knowledge of device characteristics and layout dependent effect • Experience in scripting, Perl or Tcl
Memory Layout Engineer	<ul style="list-style-type: none"> • Experience in SRAM memory instance and leaf cell layout • Experience in FinFet process node is a plus

• BS or MS degree in EE or CS related • Fresh Graduates are welcome to apply

Learning & Development

Talent cultivation and development is one of the most important mission in Faraday. We builds a comprehensive training and development system for the employees, accumulating intellectual capital and overall competitiveness.



Benefits

- Competitive Compensation
- Company Trip

- Lunch & Transport Allowance
- Year End Party

- Medical Insurance
- Annual Leave

